

FIG. 1A

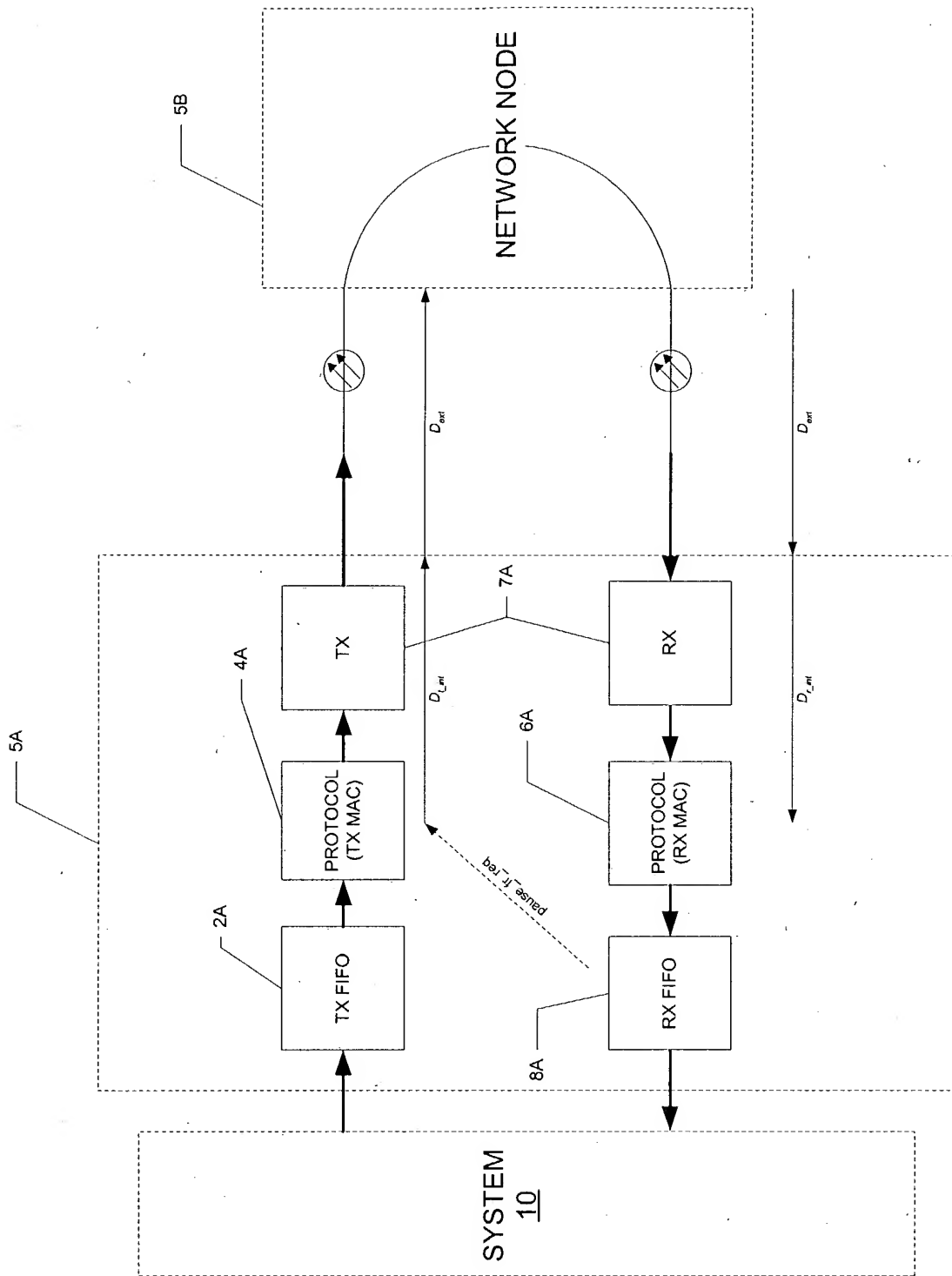


FIG. 1B

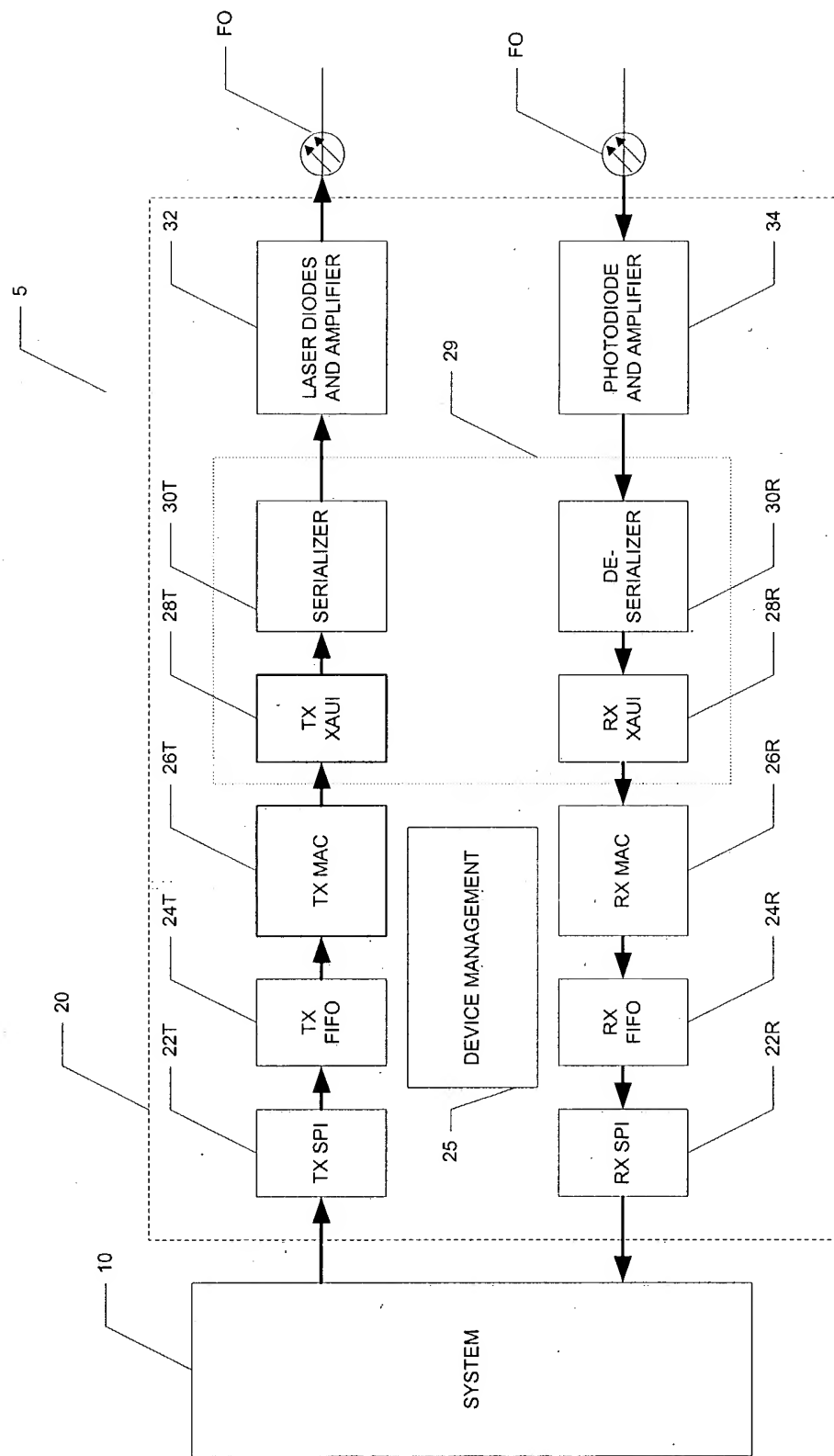


FIG. 2

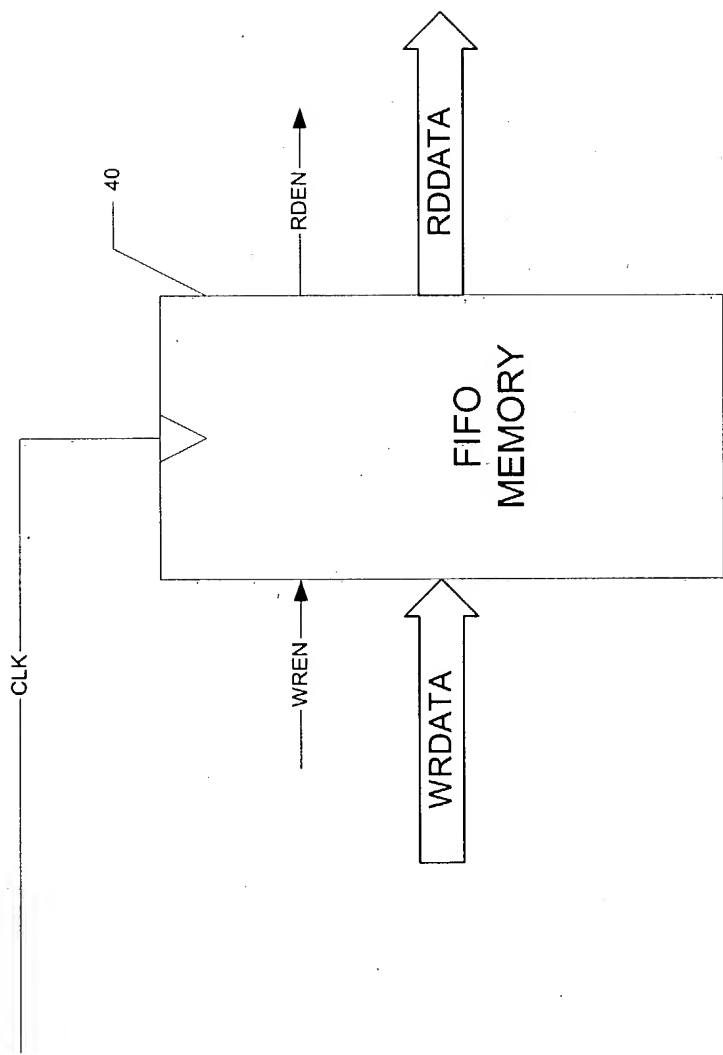


FIG. 3

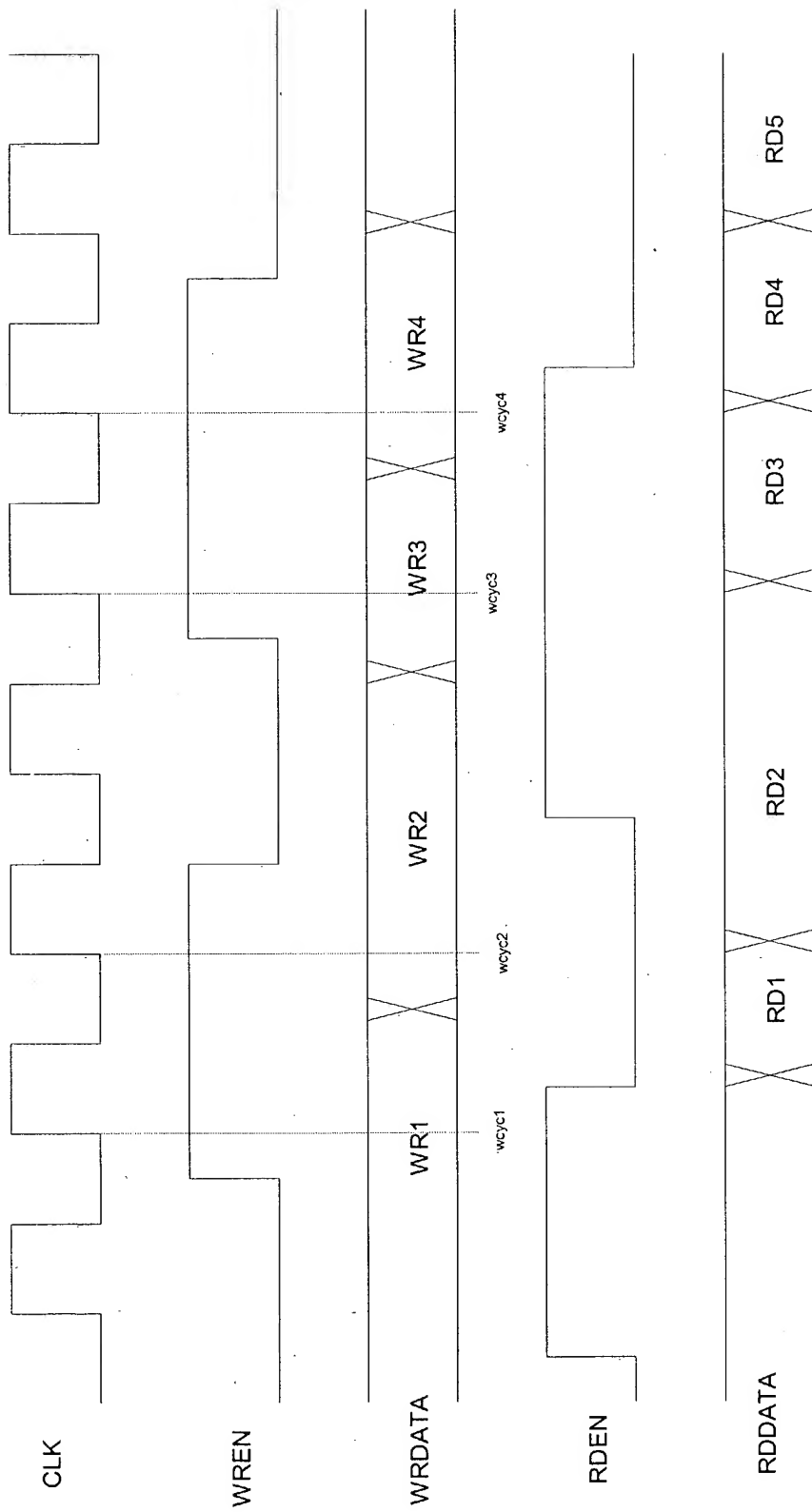


FIG. 4A

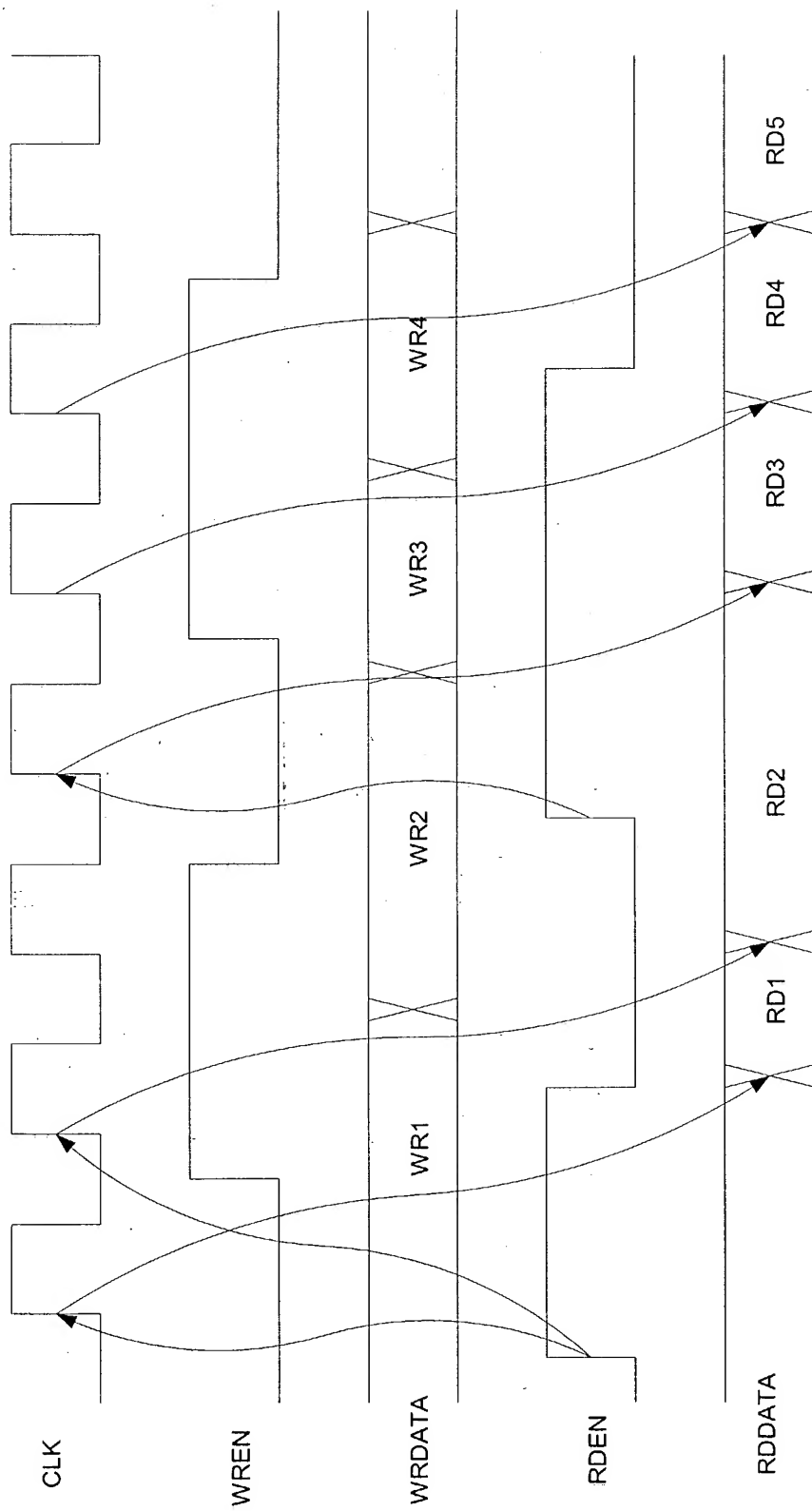
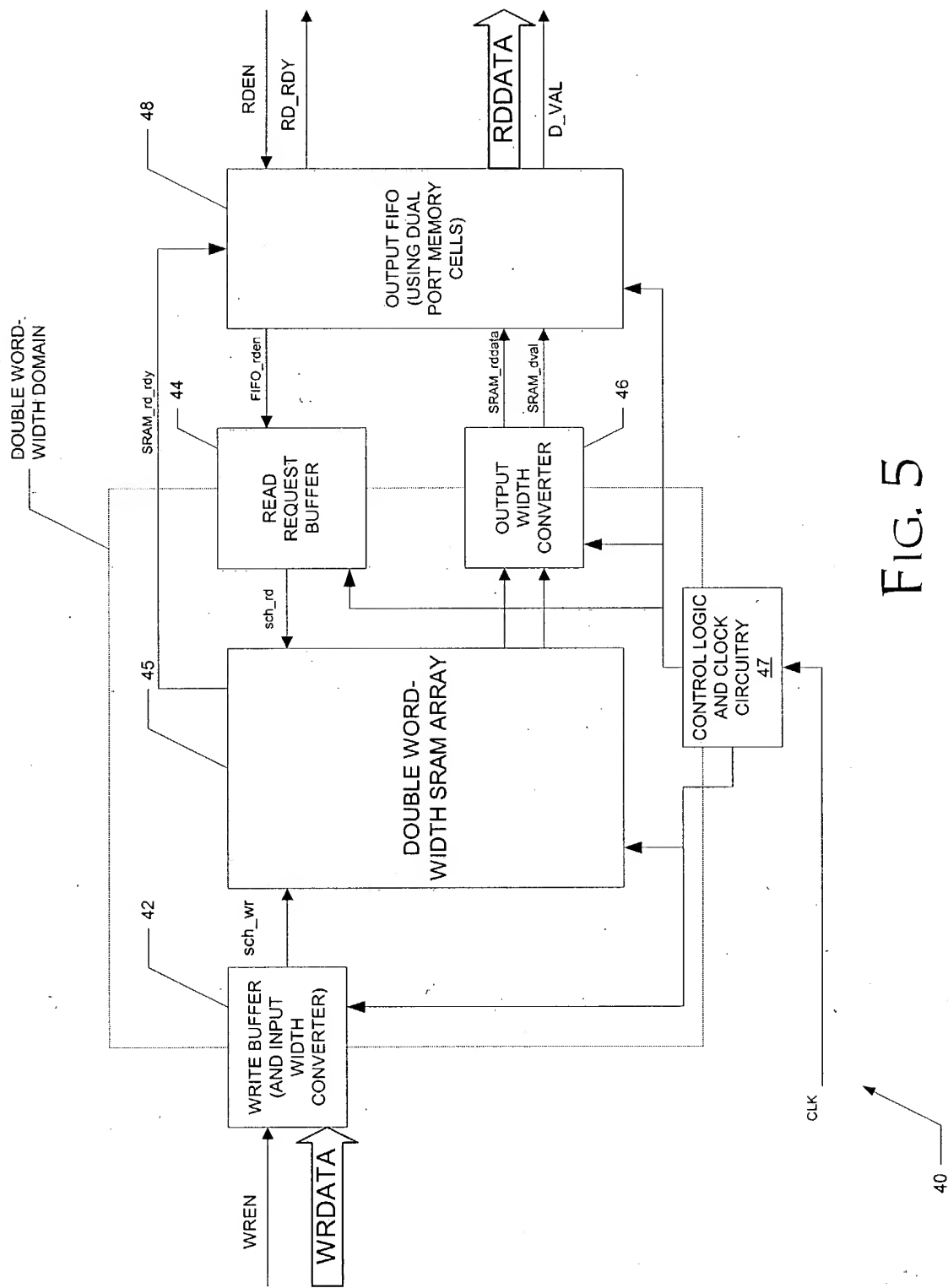


FIG. 4B



SRAM ARRAY



a0 = EMPTY
a0 = REGISTERED

FIG. 7

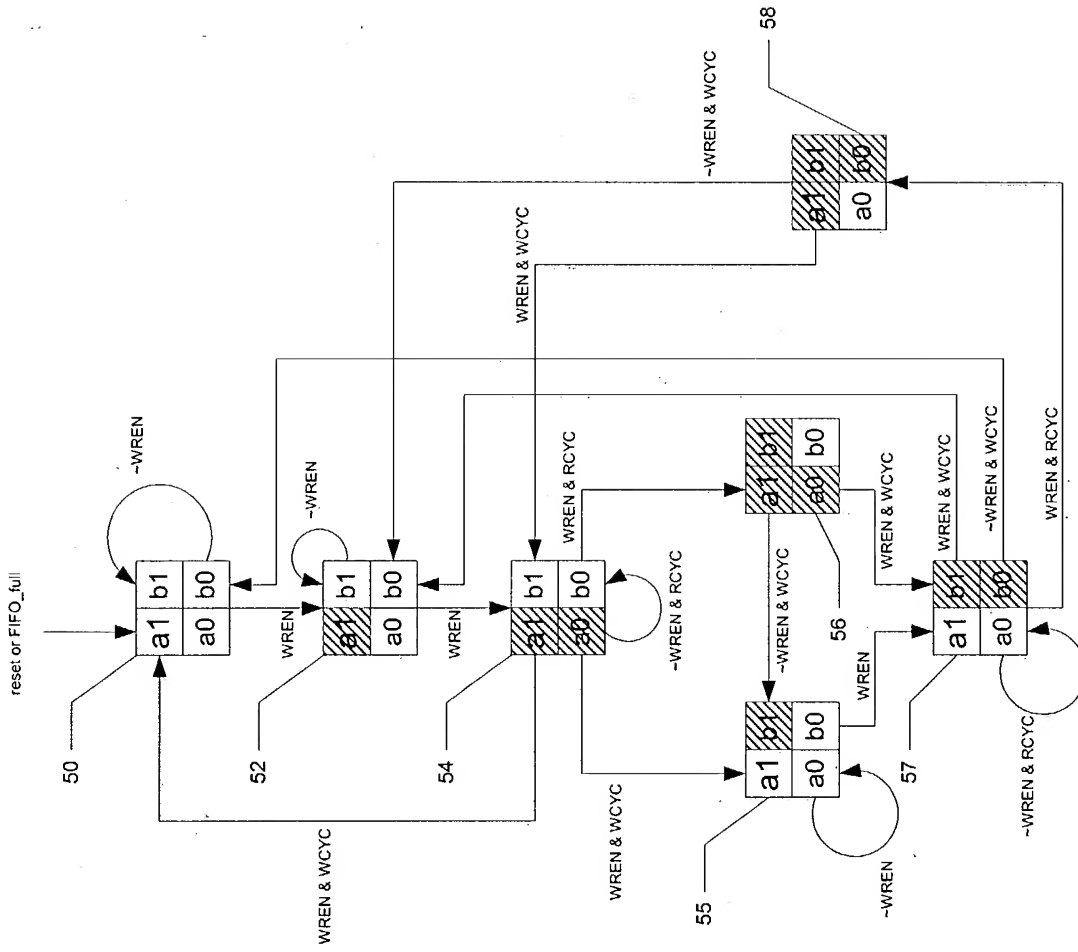


FIG. 8

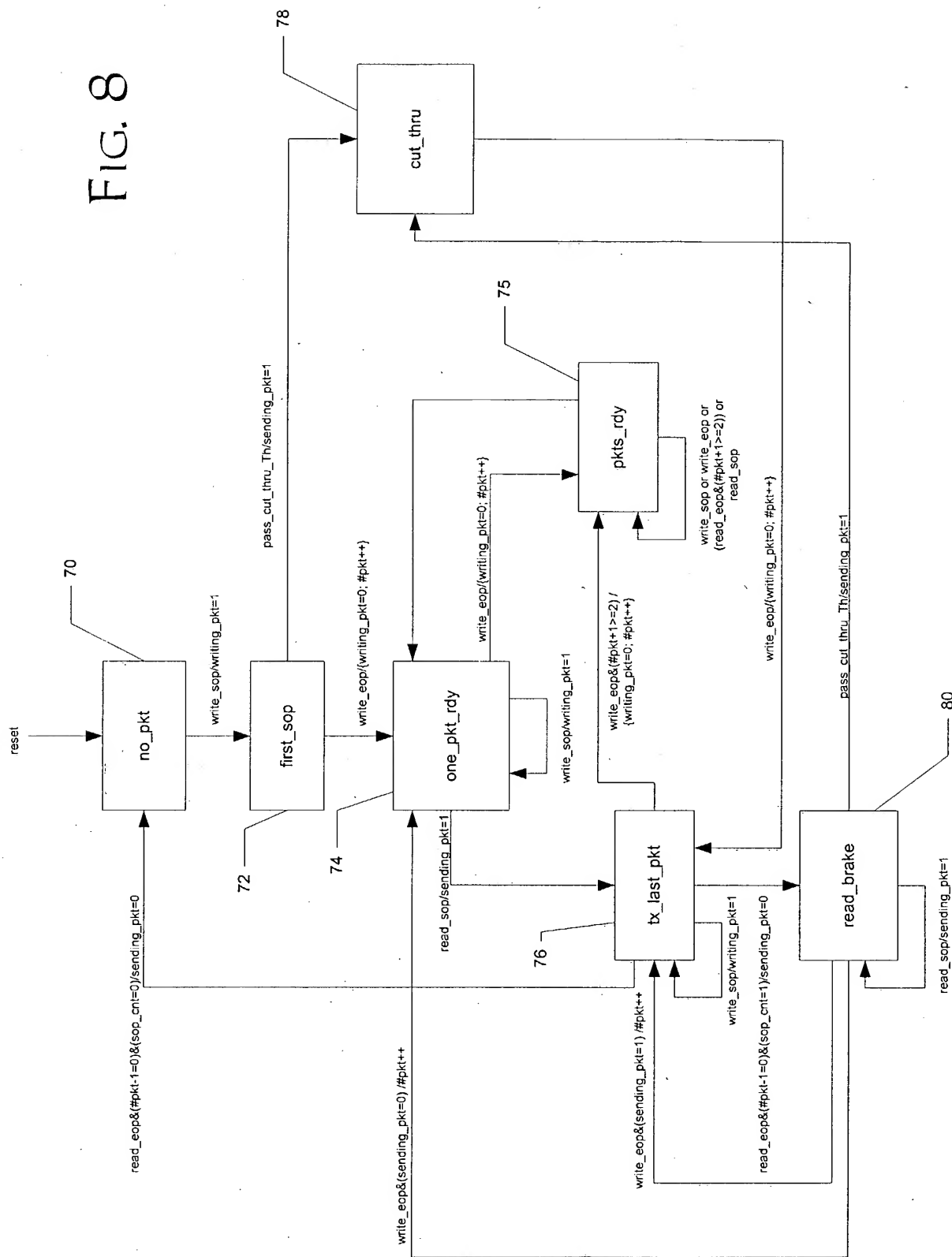
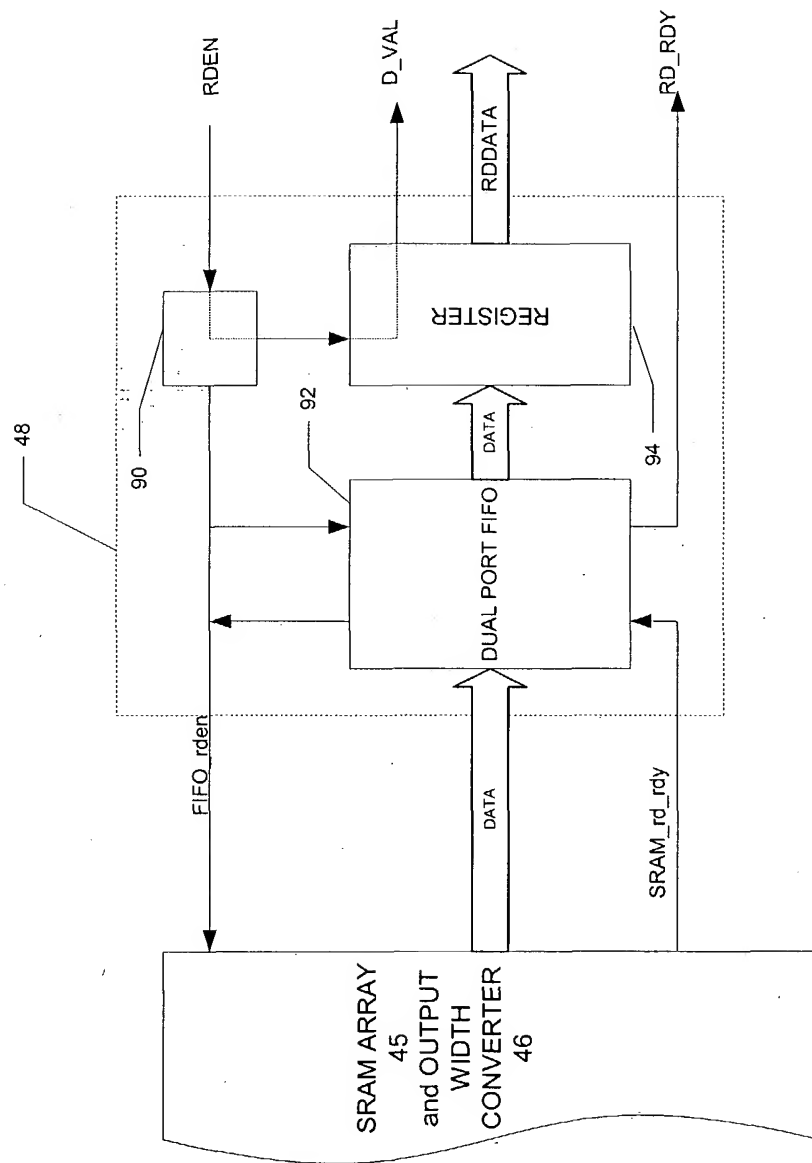


FIG. 9



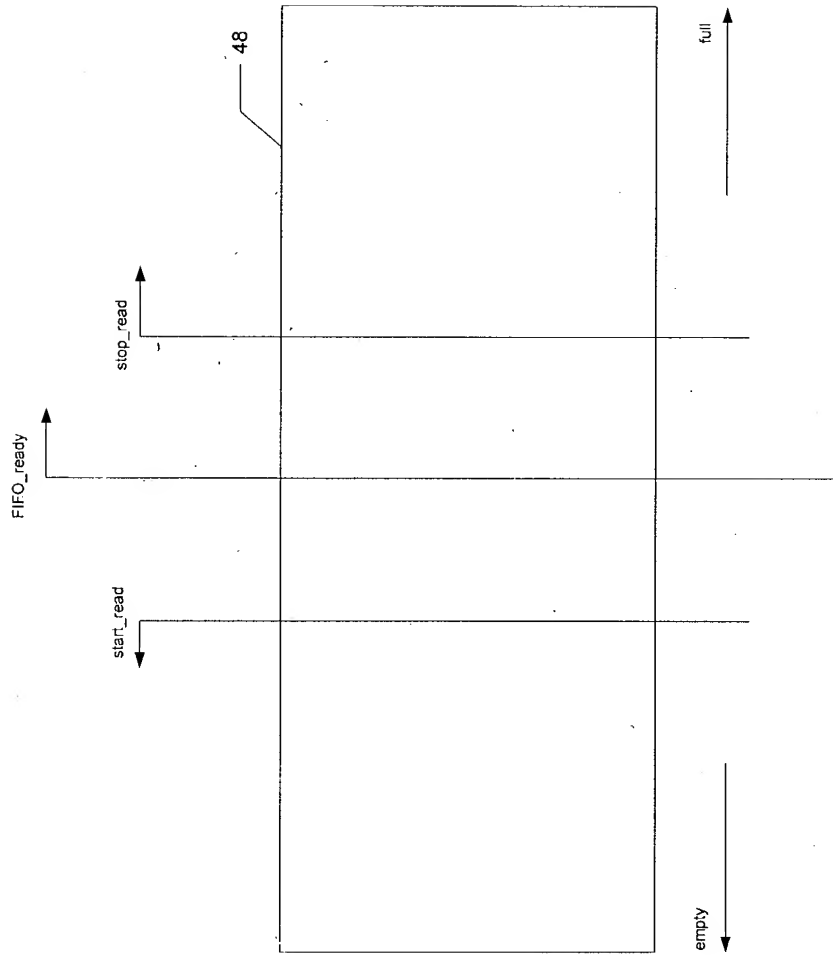


FIG. 10